



Programme of the M. E. Sem–I & Sem-II (OLD) (Branch:Electronics &Telecommunication)  
Digital Electronics Examinations Examinations Nov/Dec,2025

1. Candidates are required to be present at their respective places of the Examinations fifteen minutes before the time for paper.
2. Candidates are forbidden to take any material into the Examination Hall, with a view to prevent malpractices at the time of examinations.
3. Candidates are requested to see the Notice-Board at their respective places of examination regularly, for changes if any notified later, in the programme.
4. Candidates are requested to note the date, day, time and place of every paper on every day.
5. Candidates are permitted to use stencils at the time of examinations.
6. Candidates for the examination are expected to provide themselves with slide-rules.
7. The exchange or loan of slide-rules, drawing instruments or other materials used in the examination, is not permitted while the examination is in progress. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances.
8. The Term Work Assessment will be held at the respective Engineering College.
9. Use of non-programmable, battery operated Electronic Calculator of pockets is allowed. The Exchange of Calculator is not allowed.
- 10.No request for any special concession, such as change in time or day fixed for the University Examination on religious or any other ground, shall be granted.
- 11.University reserves right to make timely changes in examination time-table.
12. Center for the Examination will be as under...

| Sr. | Colleges                                     | Centre                                       |
|-----|----------------------------------------------|----------------------------------------------|
| 1.  | -----                                        | -----                                        |
| 2.  | -----                                        | -----                                        |
| 3.  | -----                                        | -----                                        |
| 4.  | -----                                        | -----                                        |
| 5.  | -----                                        | -----                                        |
| 6.  | -----                                        | -----                                        |
| 7.  | -----                                        | -----                                        |
| 8.  | -----                                        | -----                                        |
| 9   | -----                                        | -----                                        |
| 10  | -----                                        | -----                                        |
| 11  | NES'S Gangamai College of Engineering,Nagaon | NES'S Gangamai College of Engineering,Nagaon |
| 12  | -----                                        | -----                                        |

# TIME TABLE/ DATE SHEET

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M.E. Sem –I and Sem- II  
(Branch: Electronics &Telecommunication)  
Digital Electronics **Examinations Nov/Dec, 2025**  
**Program Code : 162372010**

## M. E. (Branch : Electronics & Telecommunication) Digital Electronics

| Day | Date | Timing | Subjects: | Sub code |
|-----|------|--------|-----------|----------|
|-----|------|--------|-----------|----------|

### SEM-I and Sem-II

|           |            |                            |                                                 |      |
|-----------|------------|----------------------------|-------------------------------------------------|------|
| Friday    | 21.11.2025 | 10.00 am<br>To<br>01.00 pm | Wireless & Mobile Communication<br>{Elective-I} | 1053 |
| Saturday  | 22.11.2025 | 10.00 am<br>To<br>01.00 pm | Advanced Digital Communication<br>{Elective-II} | 1121 |
| Monday    | 24.11.2025 | 10.00 am<br>To<br>01.00 pm | VLSI Design                                     | 1040 |
| Tuesday   | 25.11.2025 | 10.00 am<br>To<br>01.00 pm | Advanced Computer Network                       | 1110 |
| Wednesday | 26.11.2025 | 10.00 am<br>To<br>01.00 pm | Advanced Digital Signal Processing              | 1020 |
| Thursday  | 27.11.2025 | 10.00 am<br>To<br>01.00 pm | Embedded System Design                          | 1090 |
| Friday    | 28.11.2025 | 10.00 am<br>To<br>01.00 pm | Advanced Instrumentation System                 | 1010 |
| Saturday  | 29.11.2025 | 10.00 am<br>To<br>01.00 pm | Microelectronic Circuit Design                  | 1100 |
| Wednesday | 03.12.2025 | 10.00 am<br>To<br>01.00 pm | Digital System Design                           | 1030 |
| Thursday  | 04.12.2025 | 10.00 am<br>To<br>01.00 pm | Image Processing & Pattern<br>Recognition       | 1080 |

**Please Note the timings.**

**Note :- Students Should Mention Subject Code No. On Answerbook With Name of Subject.**

**13.The University reserves right to make timely changes in an examination time-table.**

KBC NMU/7B/ME/3784 /2025  
Date : 12 / 11/2025

Sd/-  
**Director**  
Board of Examinations and Evaluation